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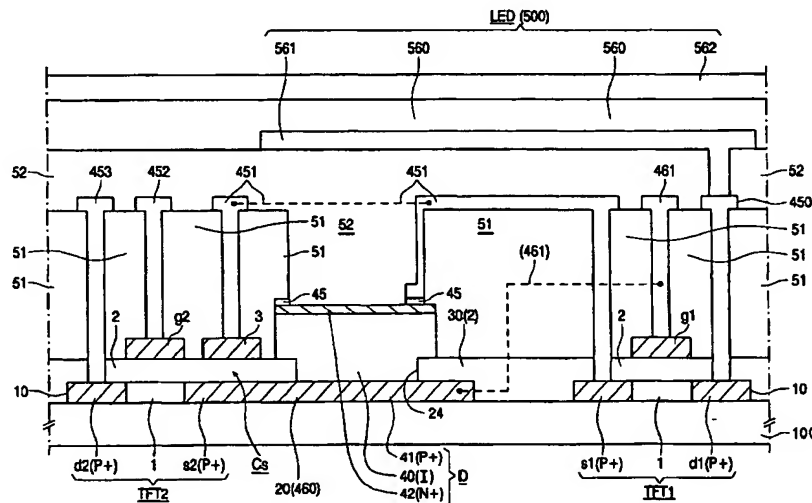
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(54) Title: MANUFACTURE OF ELECTRONIC DEVICES COMPRISING THIN-FILM CIRCUIT ELEMENTS



(57) Abstract: In the manufacture of an electronic device such as an active matrix display, a vertical amorphous PIN photodiode or similar thin-film diode (D) is advantageously integrated with a polysilicon TFT (TFT1, TFT2) in a manner that permits a good degree of optimisation of the respective TFT and diode properties while being compatible with the complex pixel context of the display. High temperature processes for making the active semiconductor film (10) of the TFT more crystalline than an active semiconductor film (40) of the diode and for forming the source and drain doped regions (s1, s2, d1, d2) of the TFT are carried out before depositing the active semiconductor film (40) of the diode. Thereafter, the lateral extent of the diode is defined by etching while protecting with an etch-stop film (30) an interconnection film (20) that can provide a doped bottom electrode region (41) of the diode as well as one of the doped regions (s2, g1) of the TFT.

INTERNATIONAL SEARCH REPORT

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

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A	US 5 930 607 A (SATOU TAKASHI) 27 July 1999 (1999-07-27) column 10, line 23 -column 15, line 36; figures 1-26 ---	1-18
A	EP 0 909 972 A (KOPIN CORP) 21 April 1999 (1999-04-21) the whole document ---	1-18
P,A	US 2003/063429 A1 (WATANABE YOSHIHIRO) 3 April 2003 (2003-04-03) the whole document ---	1-18
P,A	US 2002/191140 A1 (FUJIKAWA SHINSUKE ET AL) 19 December 2002 (2002-12-19) the whole document -----	1-18

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